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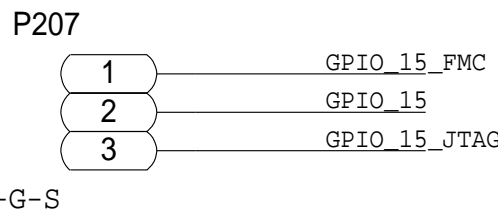
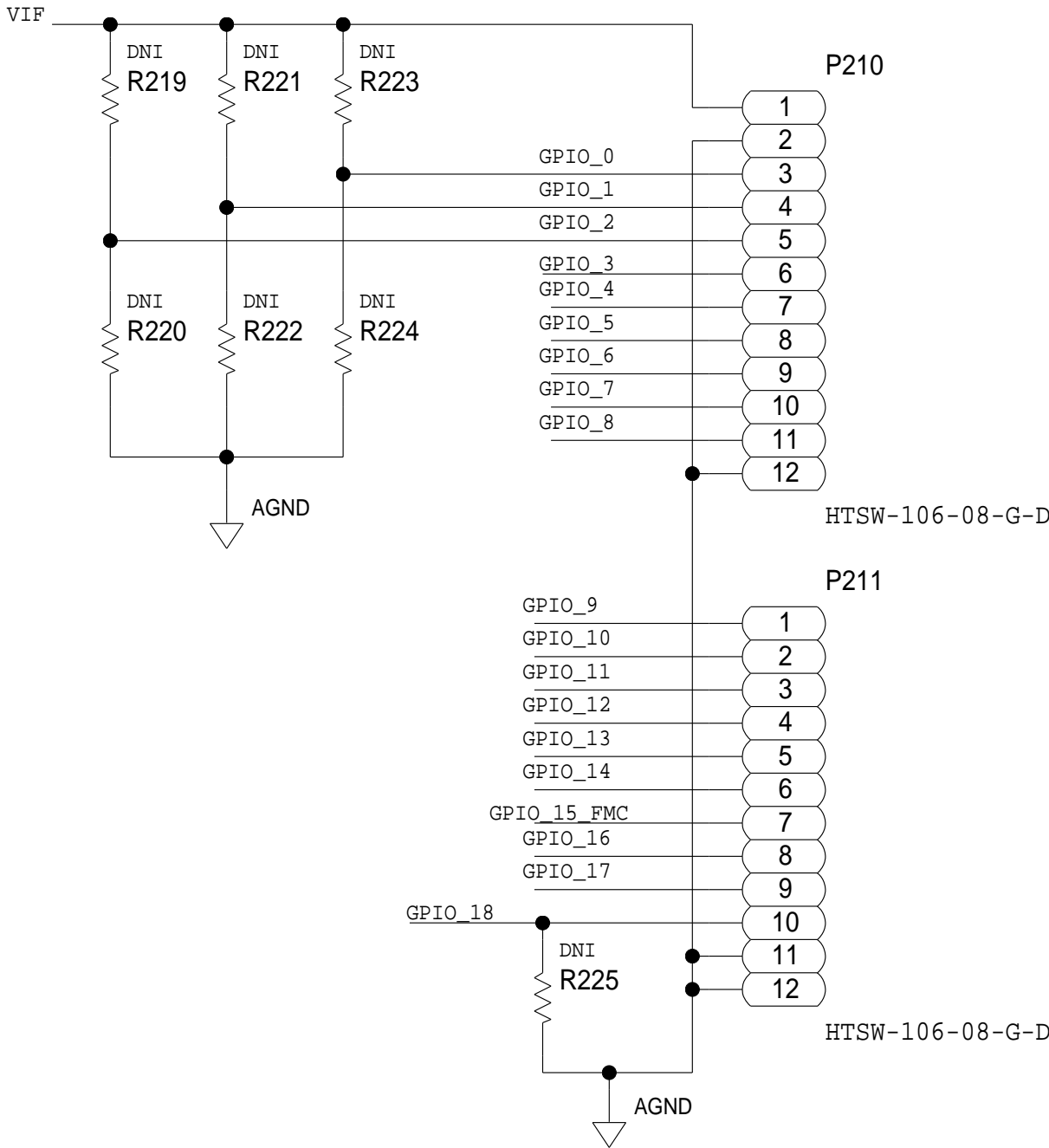
This figure is a detailed schematic diagram for the digital I/O section of a circuit, organized into a grid with columns 1 through 8 and rows A through D. The diagram includes several key components and interfaces:

- DIGITAL I/O:** Features a large title at the top left. It shows connections for various digital signals (e.g., RX1_ENABLE, TX1_ENABLE, GPINT1, GPINT2) to a microcontroller (U101, ADRV9029BBCZ) and other components like level shifters (U201, U202) and buffers (U203).
- AUXDAC OUTPUTS AND 1.8V GPIO:** Shows connections for auxiliary DAC outputs (AUXADC_0 to AUXADC_3) and 1.8V GPIO signals (GPIO_ANA_0 to GPIO_ANA_7) to a microcontroller (U101) and other components like capacitors (C201 to C208) and resistors (R201 to R212).
- DIGITAL GPIO:** Shows connections for digital GPIO signals (GPIO_0 to GPIO_18) to a microcontroller (U101) and other components like capacitors (C209 to C212) and resistors (R213 to R225).
- AUXADC INPUTS:** Shows connections for auxiliary ADC inputs (AUXADC_0 to AUXADC_3) to a microcontroller (U101) and other components like capacitors (C209 to C212) and resistors (R213 to R225).
- SPI INTERFACE BUFFER:** Shows connections for SPI signals (SPI_CS0, SPI_DIN, SPI_DOUT, SPI_CLK) to a microcontroller (U101) and other components like capacitors (C213 to C216) and resistors (R226 to R231).
- BOARD ID EEPROM:** Shows connections for board ID EEPROM signals (EPROM_A0, EPROM_A1, FMCA_SCL) to a microcontroller (U101) and other components like capacitors (C214 to C215) and resistors (R232 to R237).
- JTAG INTERFACE:** Shows connections for JTAG signals (JTAG_TMS, JTAG_TCLK, JTAG_TDO, JTAG_TDI, JTAG_TRSTB) to a microcontroller (U101) and other components like capacitors (C218 to C219) and resistors (R227 to R231).

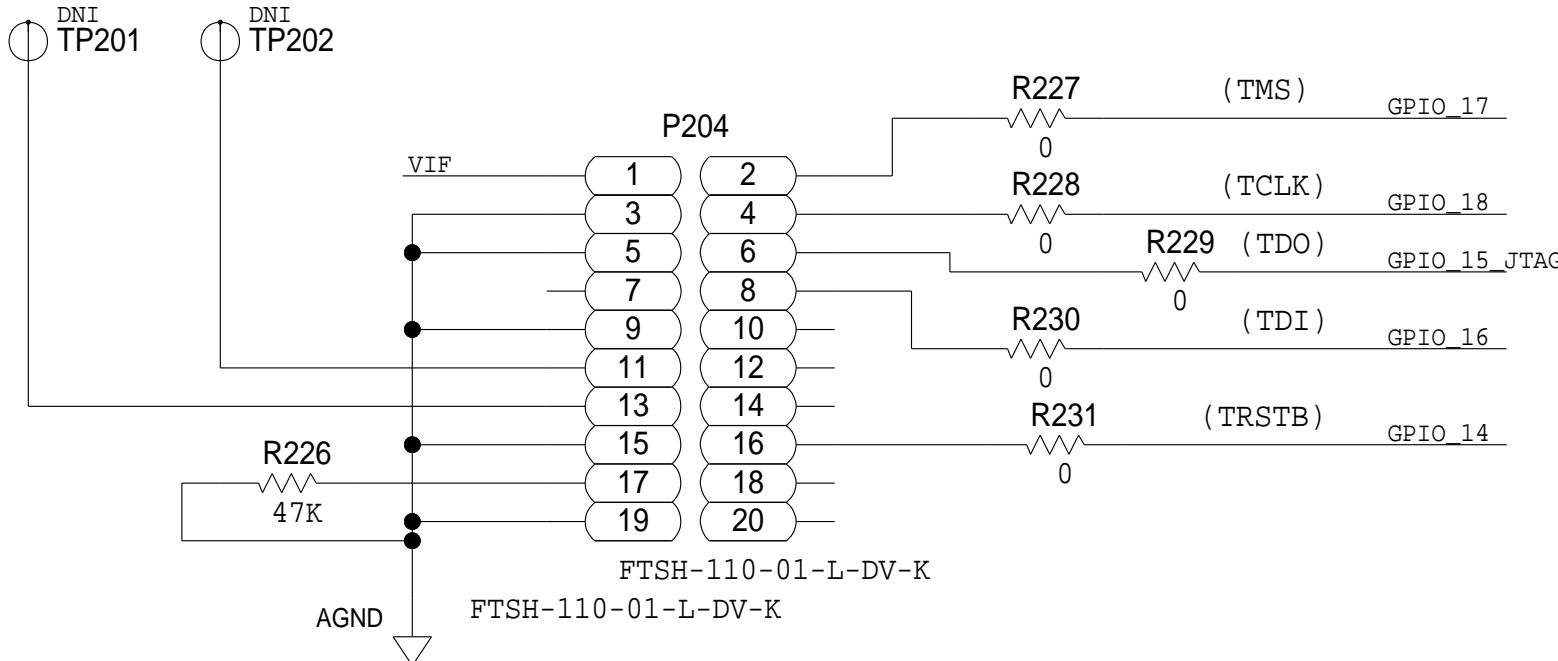
The schematic is a complex interconnection of these components, with various signal lines, power planes, and ground connections. It is a professional engineering drawing, likely for a PCB layout or manufacturing.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

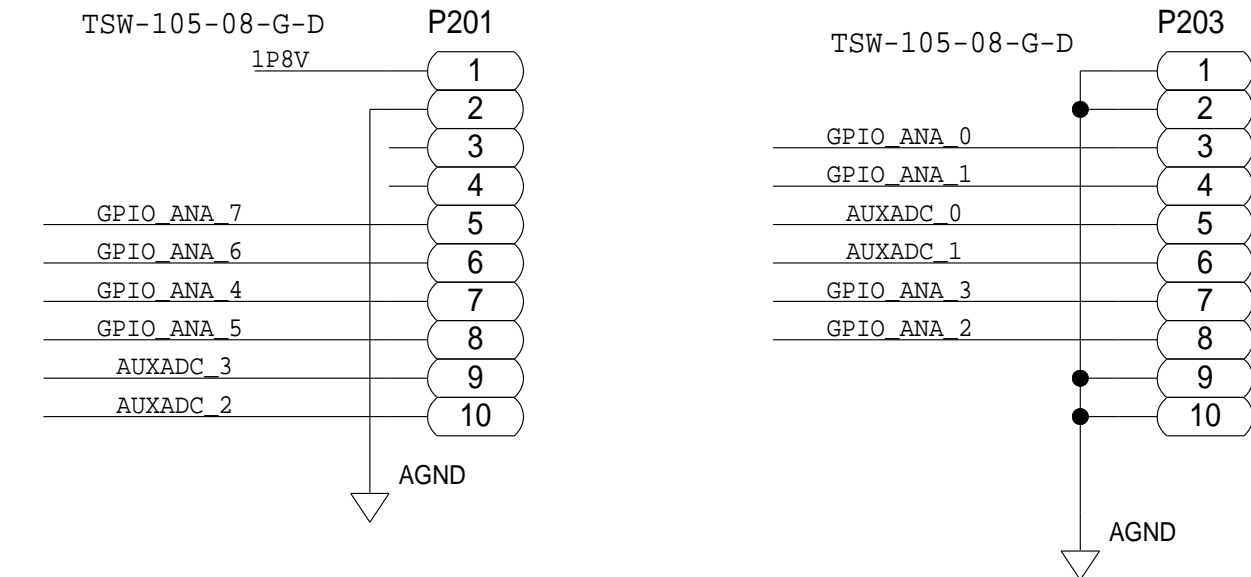
DIGITAL GPIO



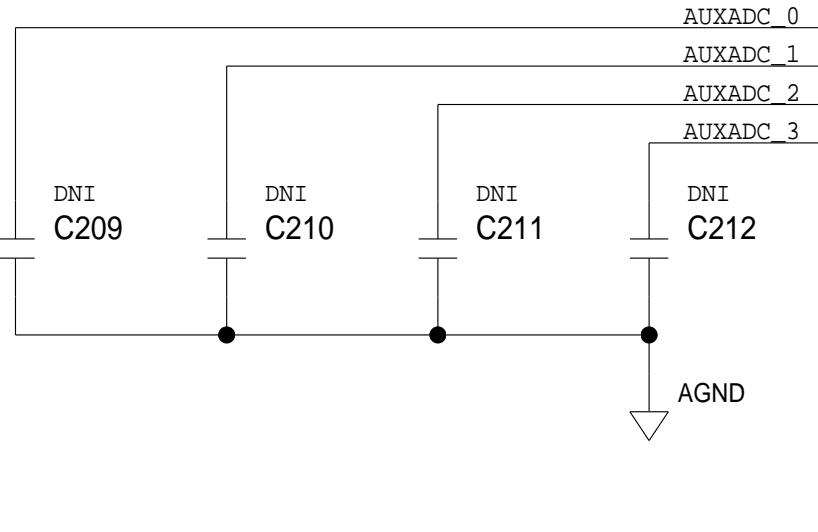
JTAG INTERFACE



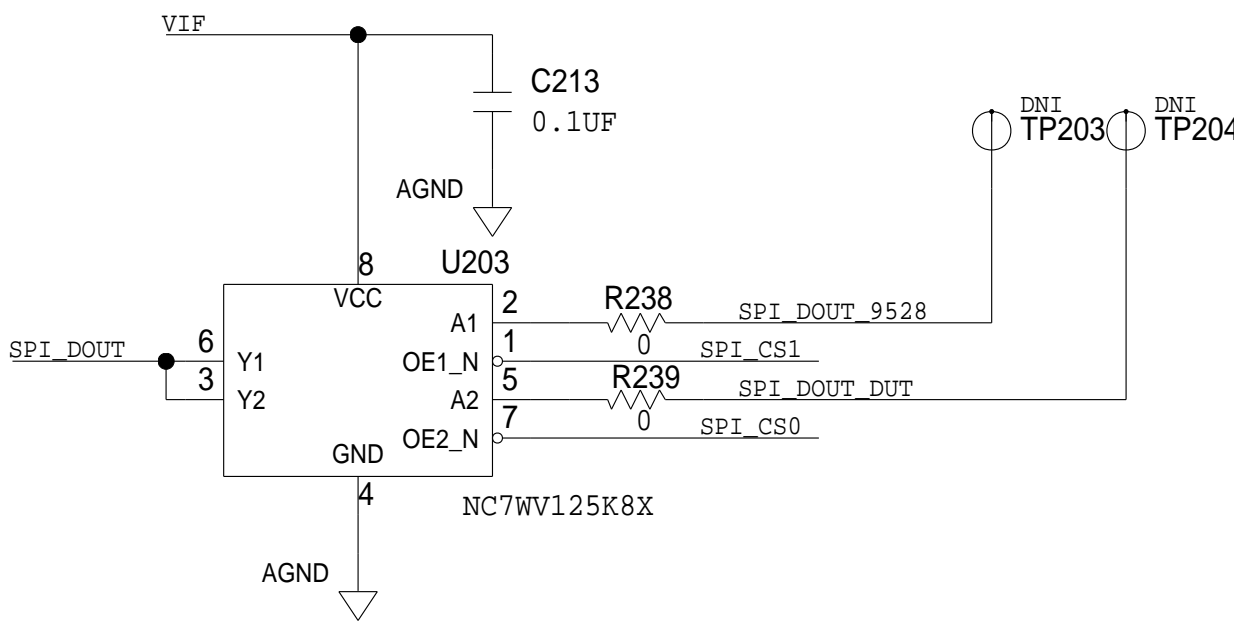
AUXDAC OUTPUTS AND 1.8V GPIO



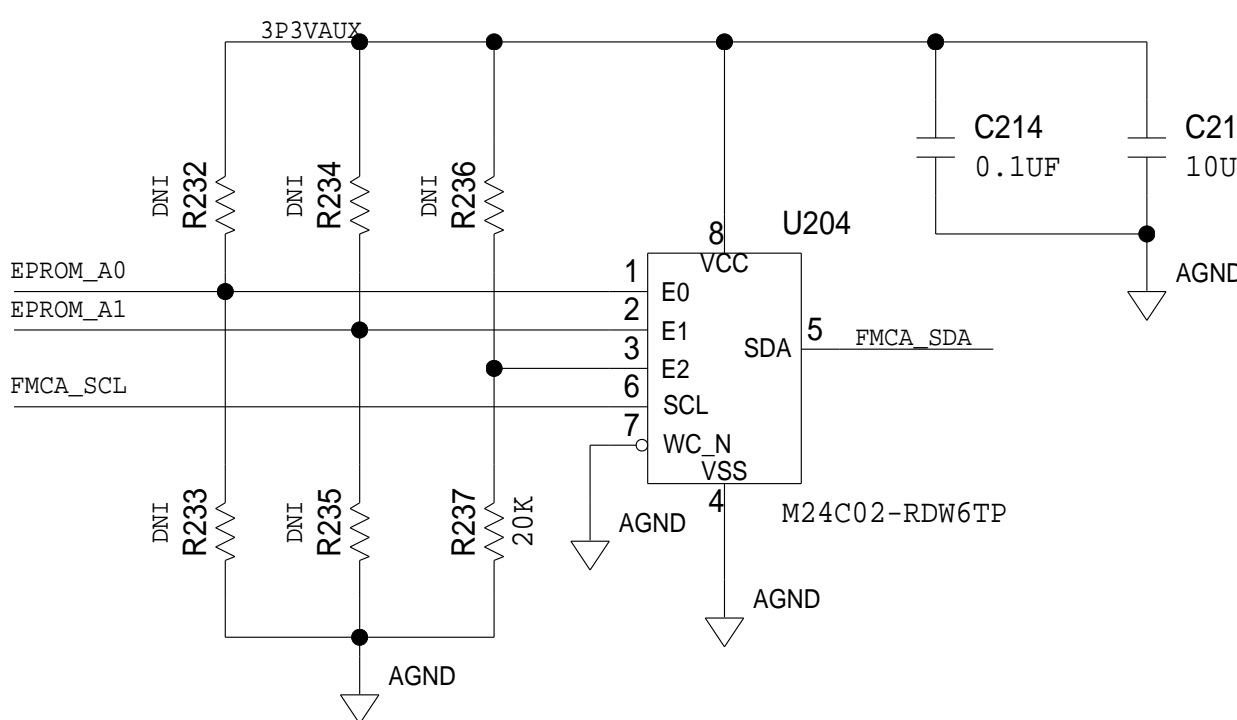
AUXADC INPUTS



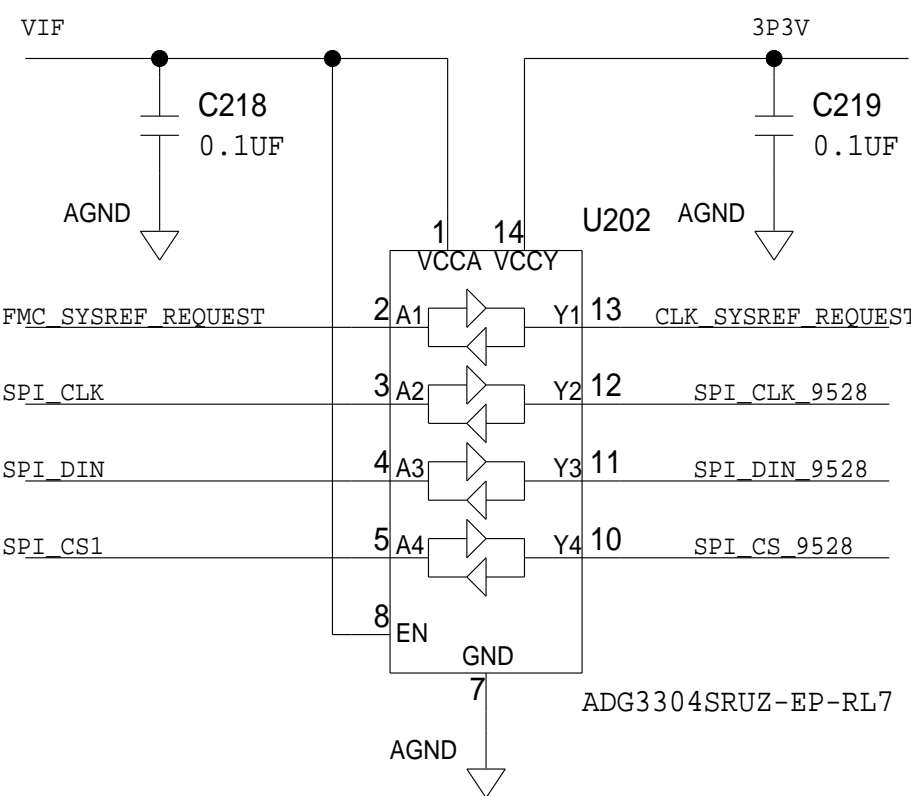
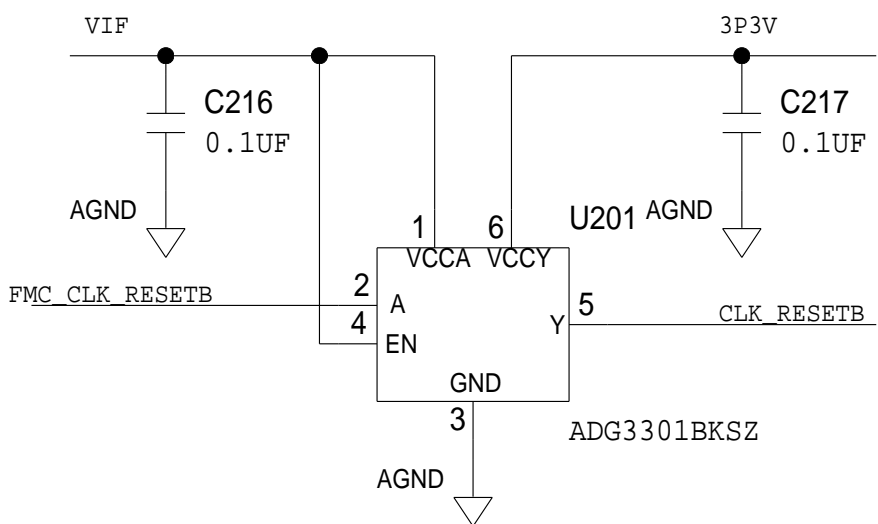
SPI INTERFACE BUFFER

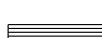


BOARD ID EEPROM



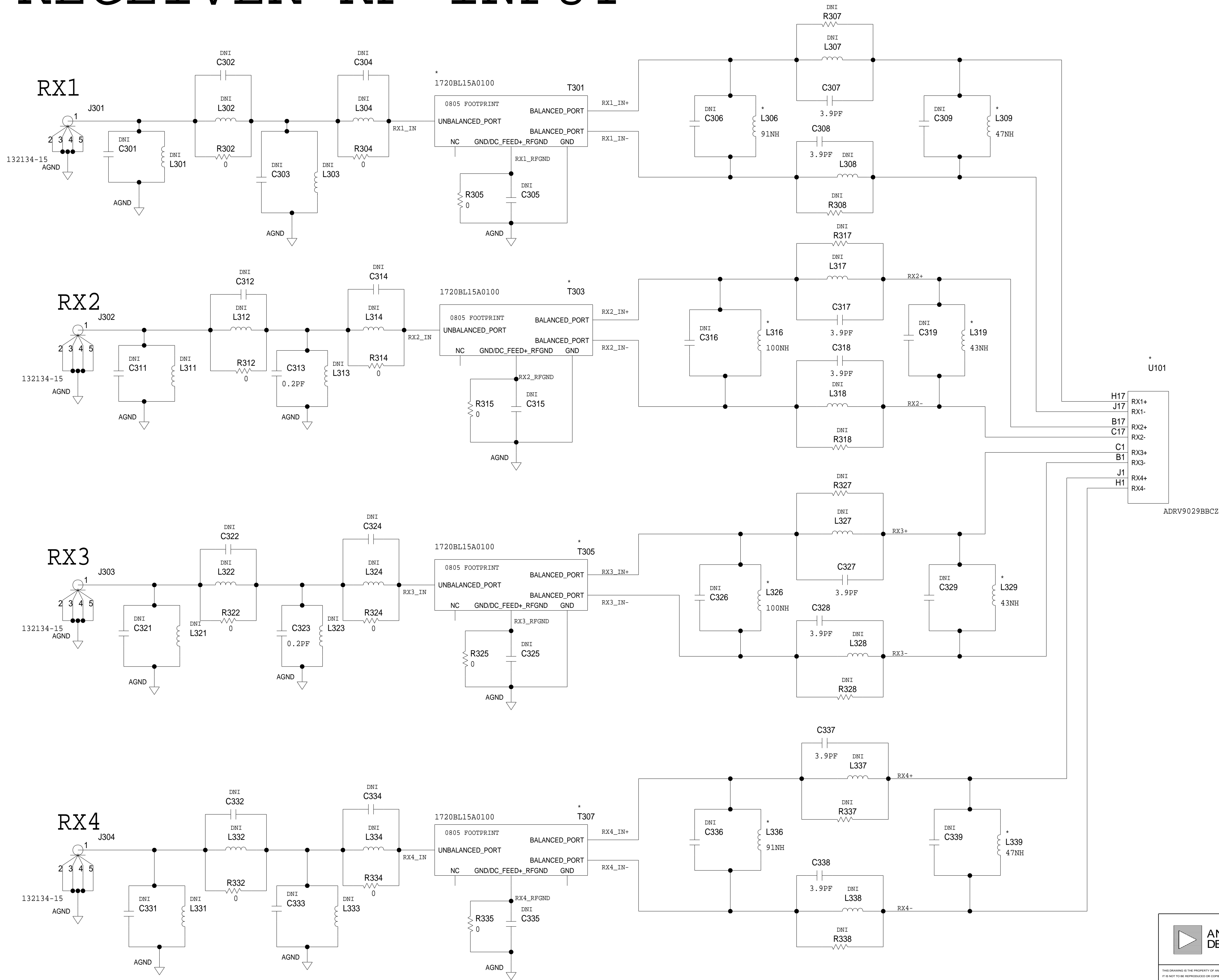
LEVEL SHIFTERS



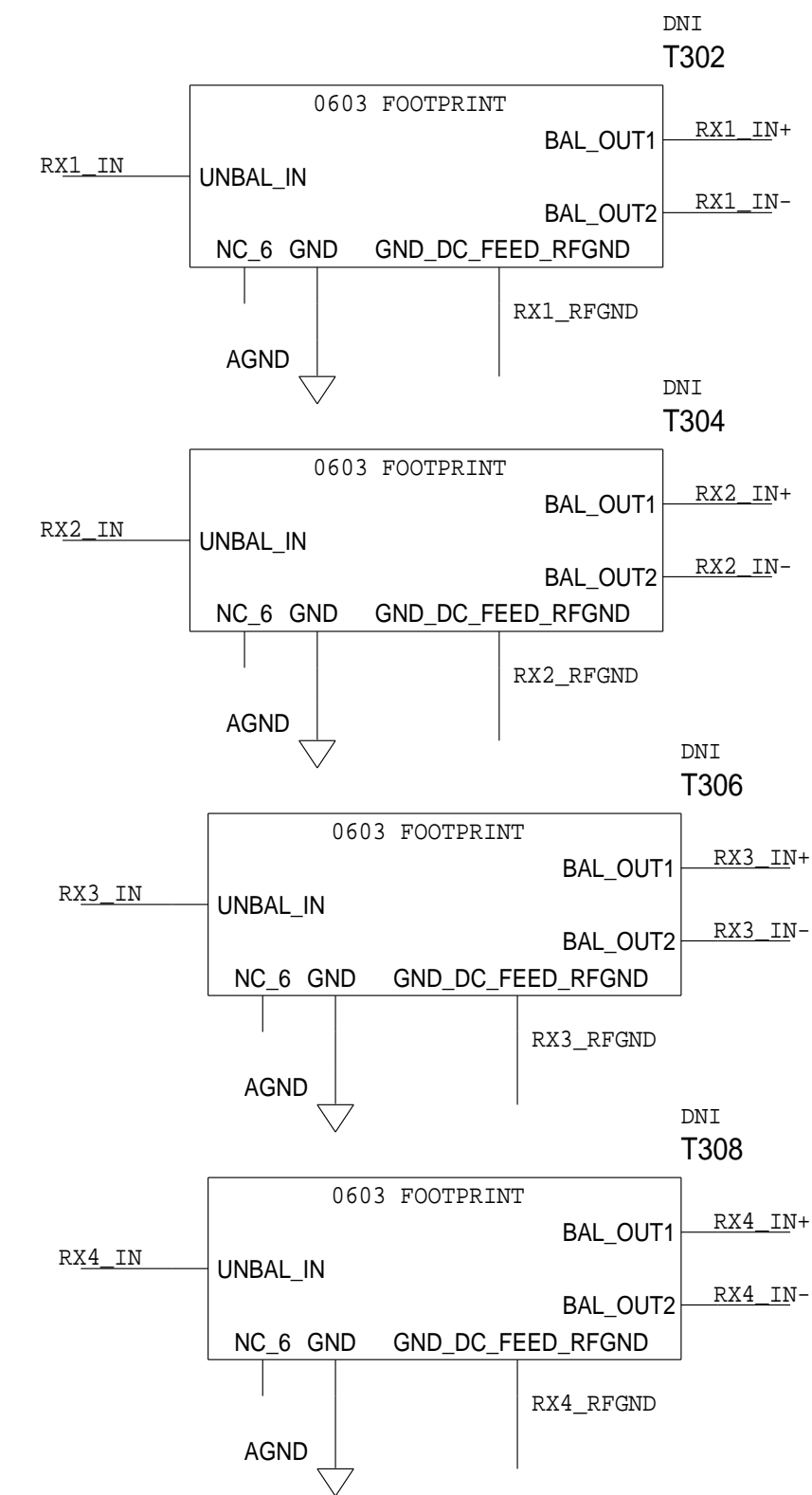
 <div>ANALOG DEVICES</div>	SCHEMATIC				
	HW TYPE : Customer Evaluation Product (s) : ADRV9025, ADRV9026, ADRV9029 : ADRV9029-MB				
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR FOR USE IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.</small>	DESIGN VIEW : ADRV9029-MB		DRAWING NO. 02_049168-06		REV C
	PTD ENGINEER R. MYERS		SIZE D	SCALE 1:1	SHEET 2 OF 10

RECEIVER RF INPUT

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



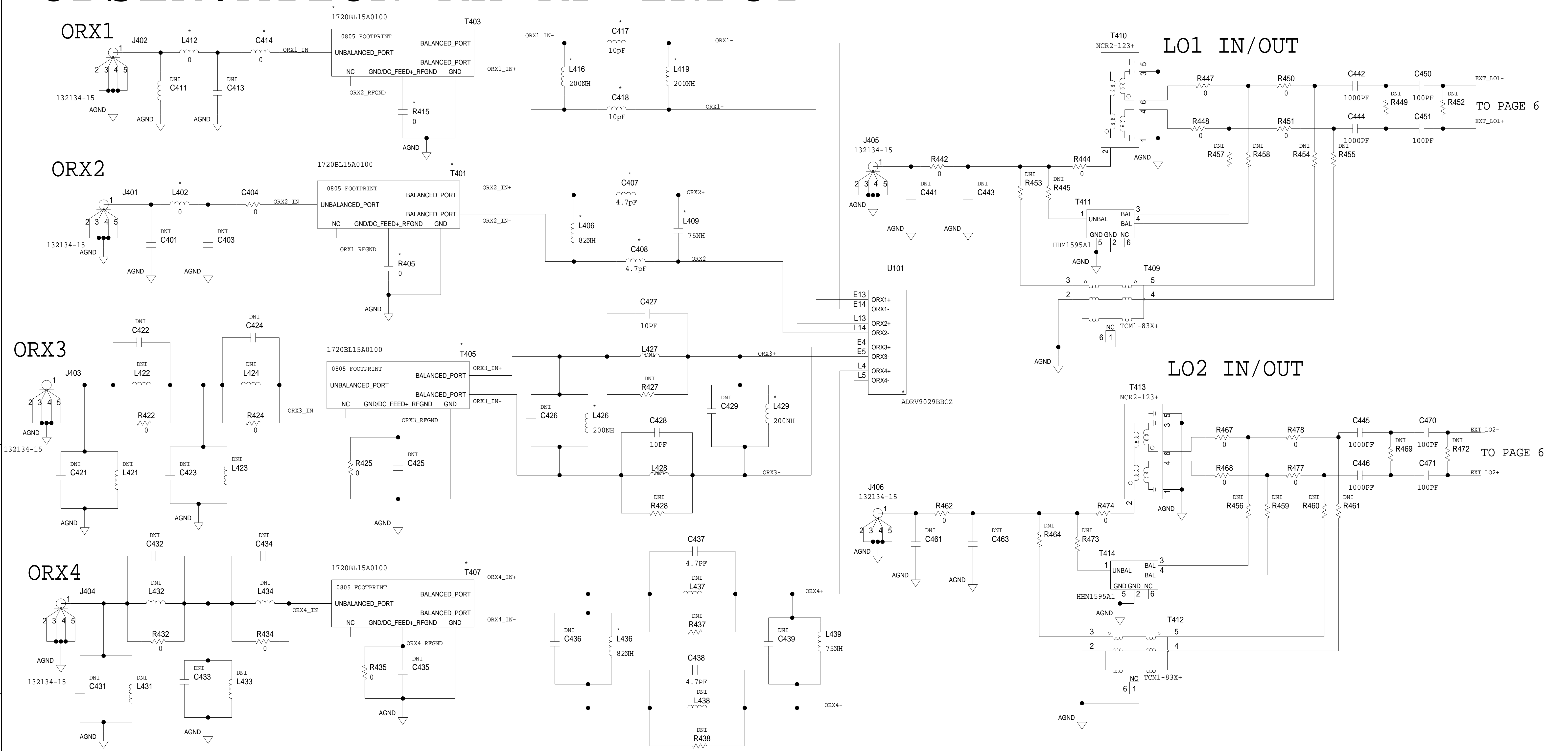
OVERLAP RX BALUNS



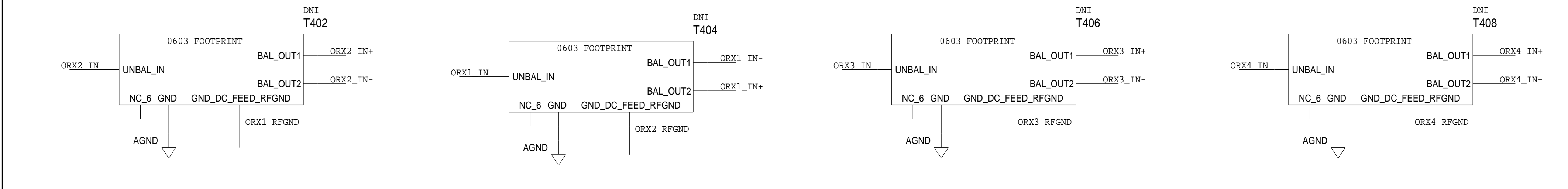
SCHEMATIC			
HW TYPE : Customer Evaluation Product (s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9029-MB			
DESIGN VIEW : ADRV9029-MB	DRAWING NO. 02_049168-06	REV C	
PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 3 OF 10

OBSERVATION RX RF INPUT

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



OVERLAP ORX BALUNS



SCHEMATIC			
HW TYPE : Customer Evaluation Product (s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9029-MB			
DESIGN VIEW : ADRV9029-MB	DRAWING NO. 02_049168-06	REV C	
PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 4 OF 10

TRANSMITTER RF OUTPUT

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

* U101	
TX1+	N17 TX1_OUT+
TX1-	P17 TX1_OUT-
TX2+	A13 TX2_OUT+
TX2-	A14 TX2_OUT-
TX3+	A4 TX3_OUT+
TX3-	A5 TX3_OUT-
TX4+	P1 TX4_OUT+
TX4-	N1 TX4_OUT-

ADRV9029BBCZ

RF OUTPUT 1

RF OUTPUT 2

RF OUTPUT 3

RF OUTPUT 4

OVERLAP TX BALUNS

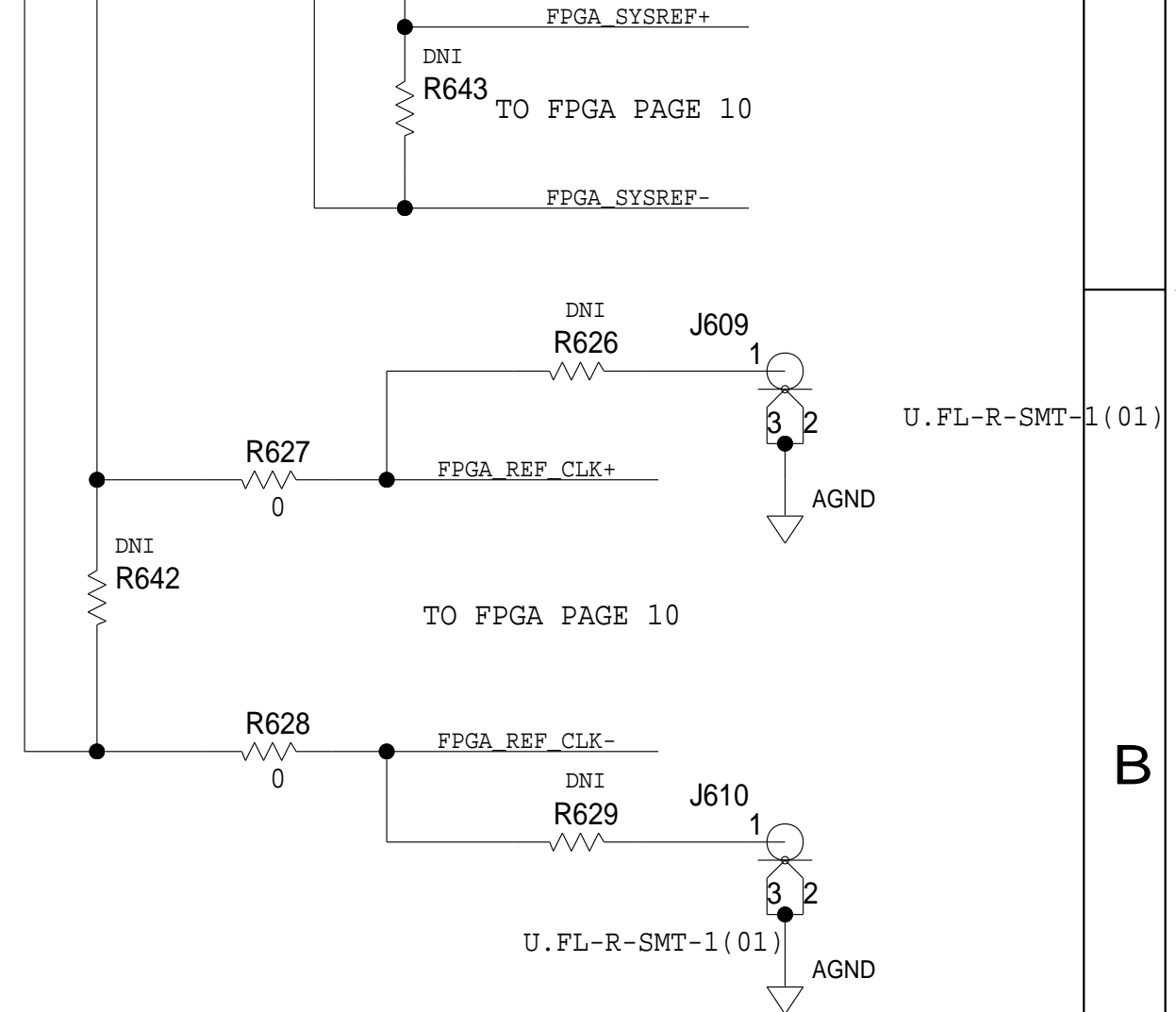
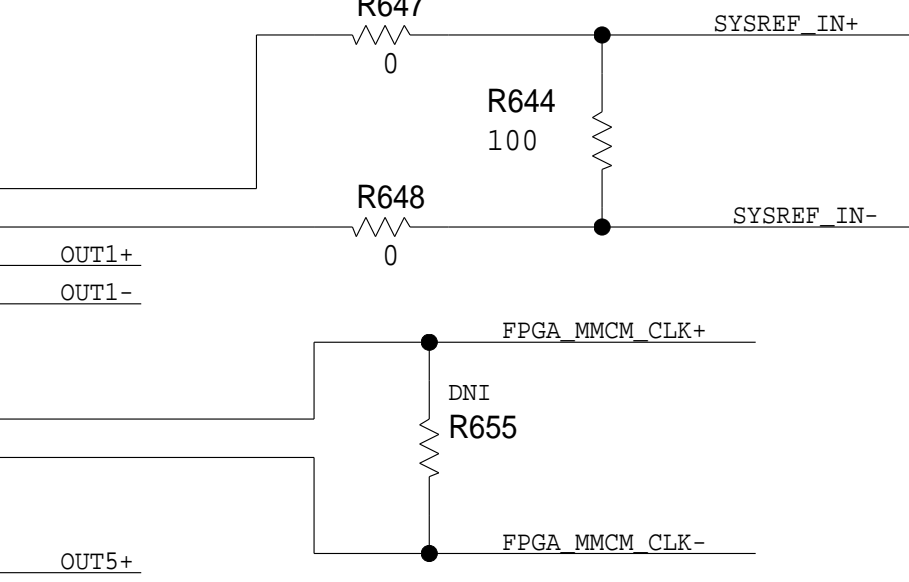
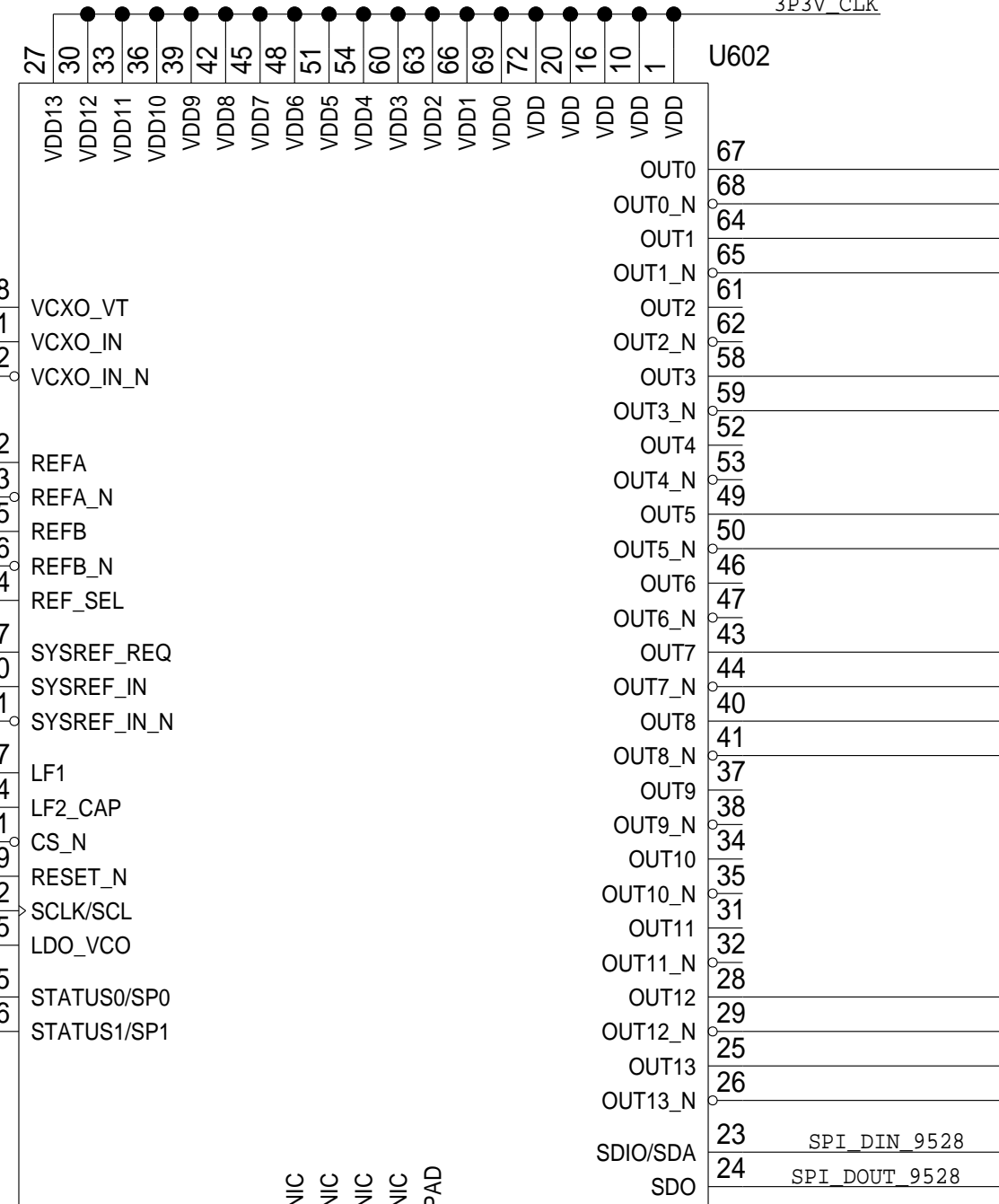
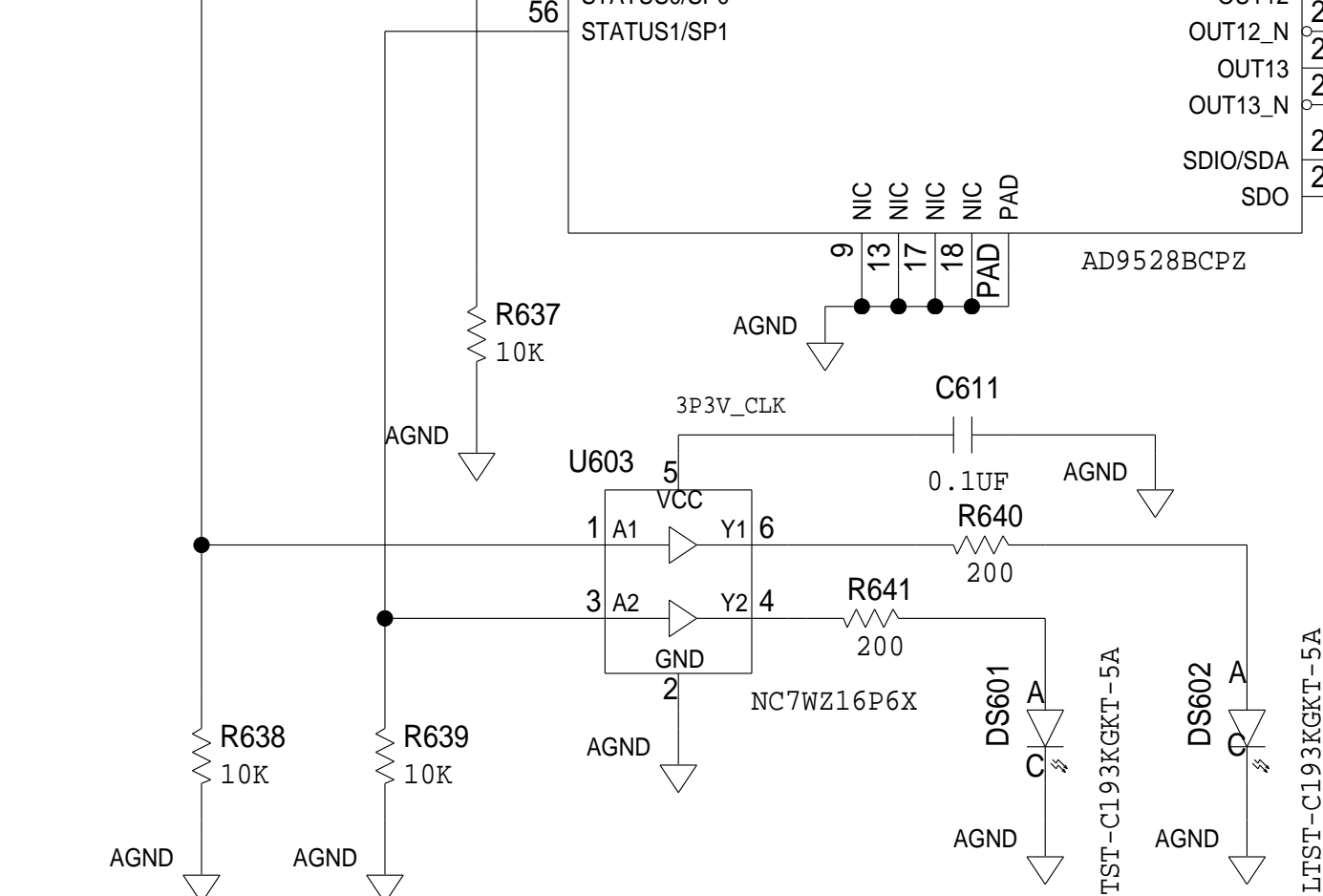
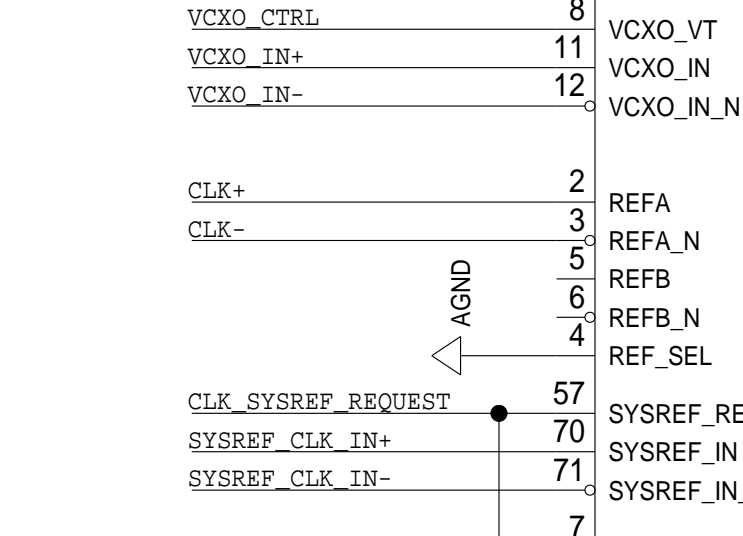
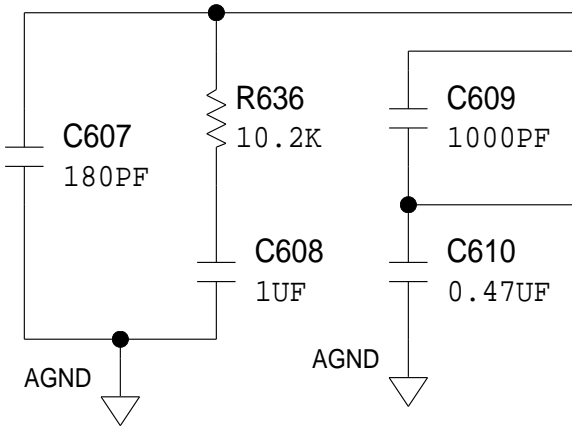
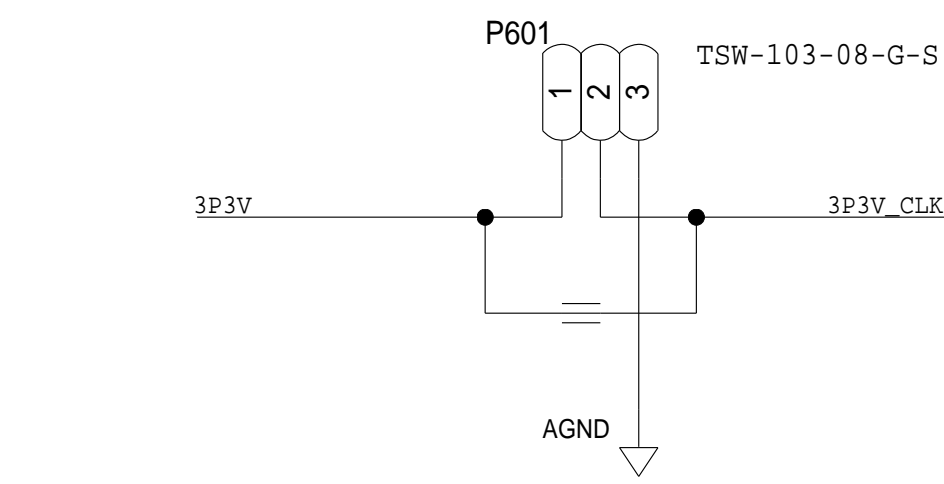
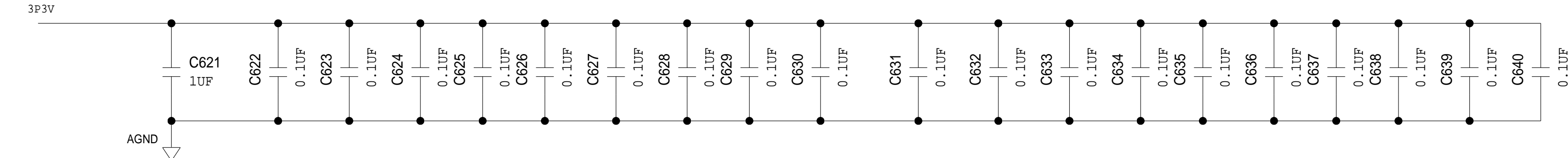
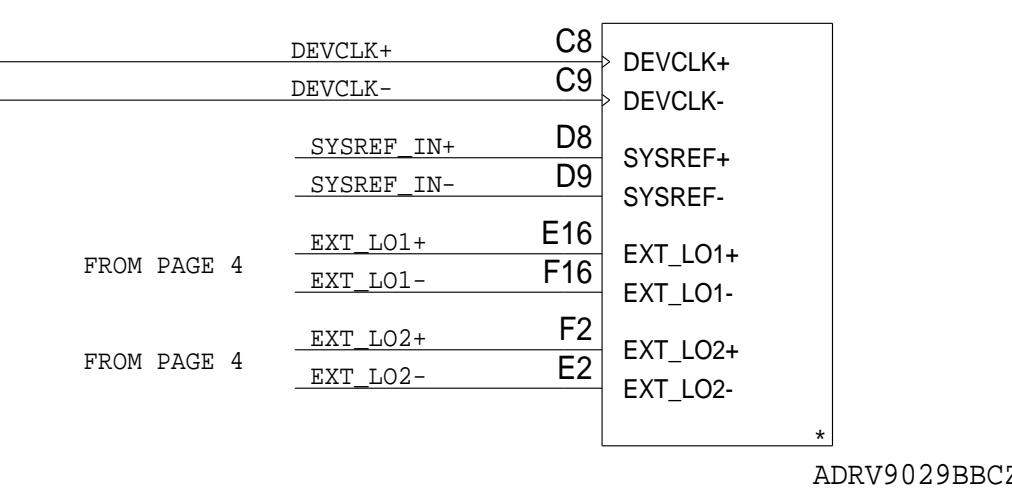
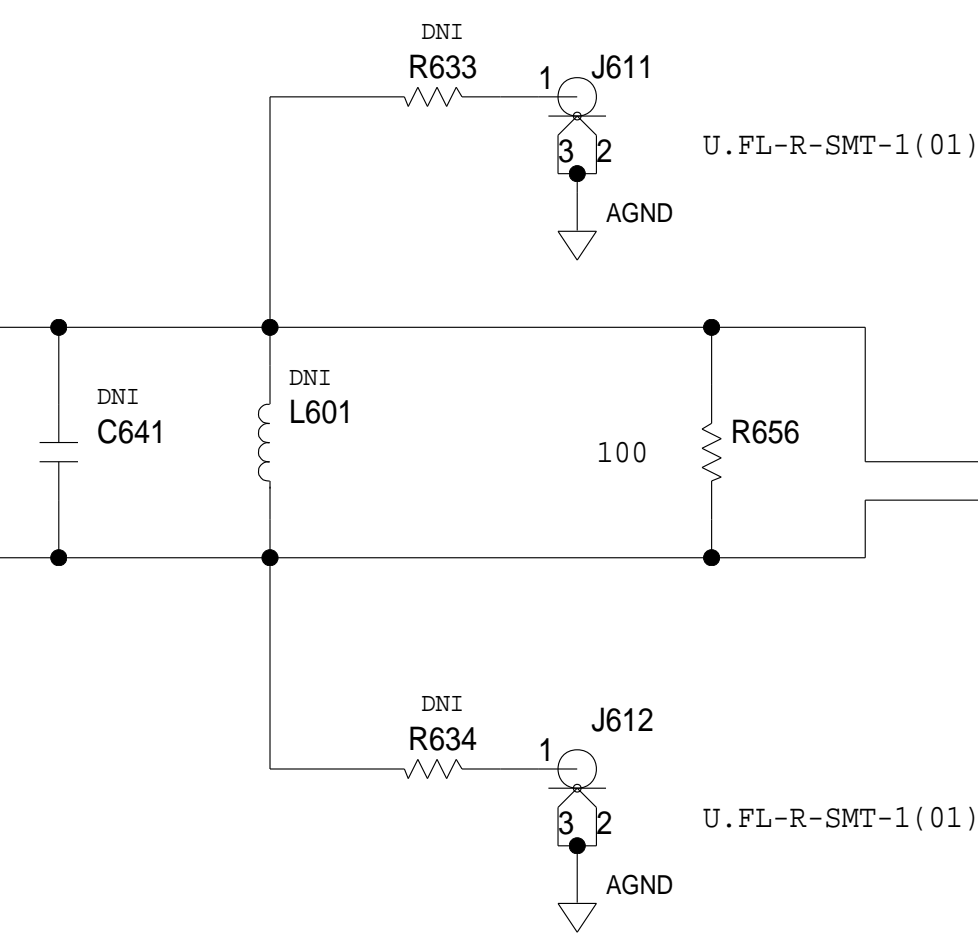
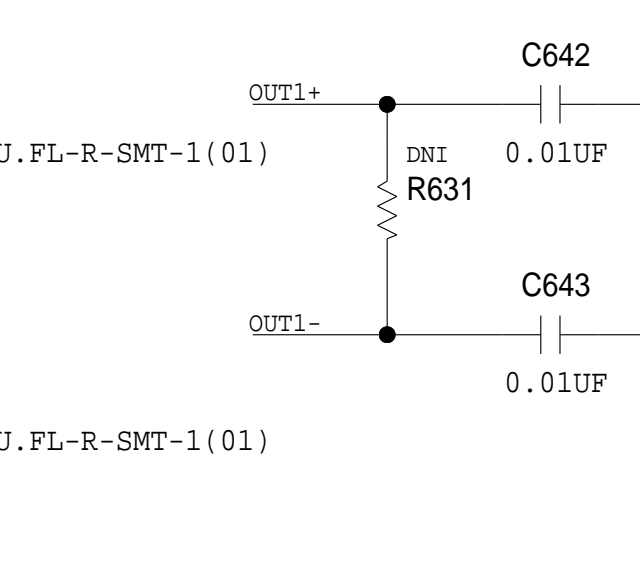
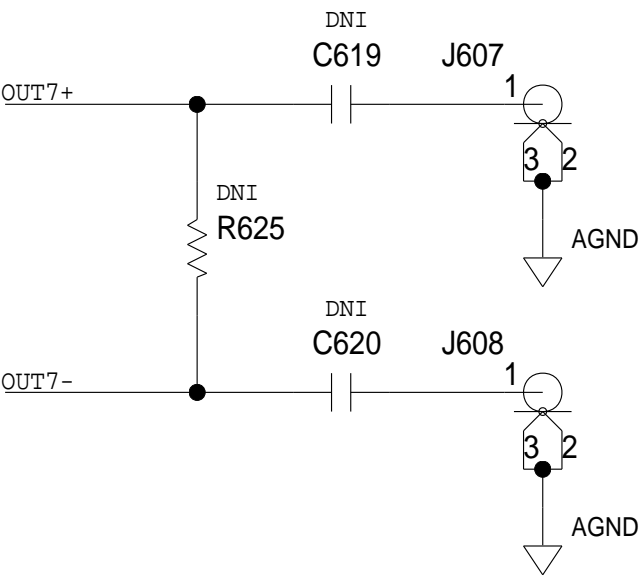
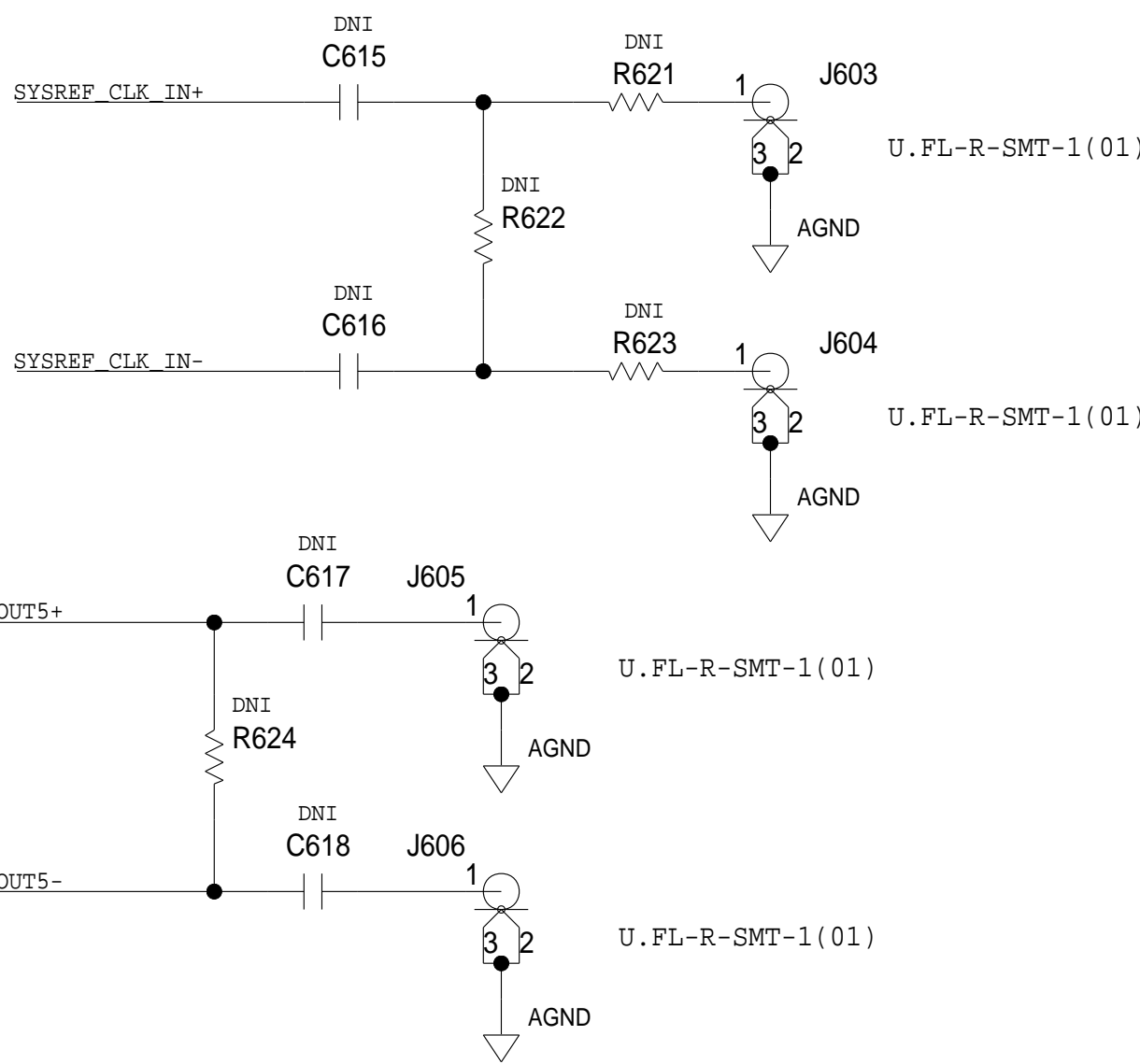
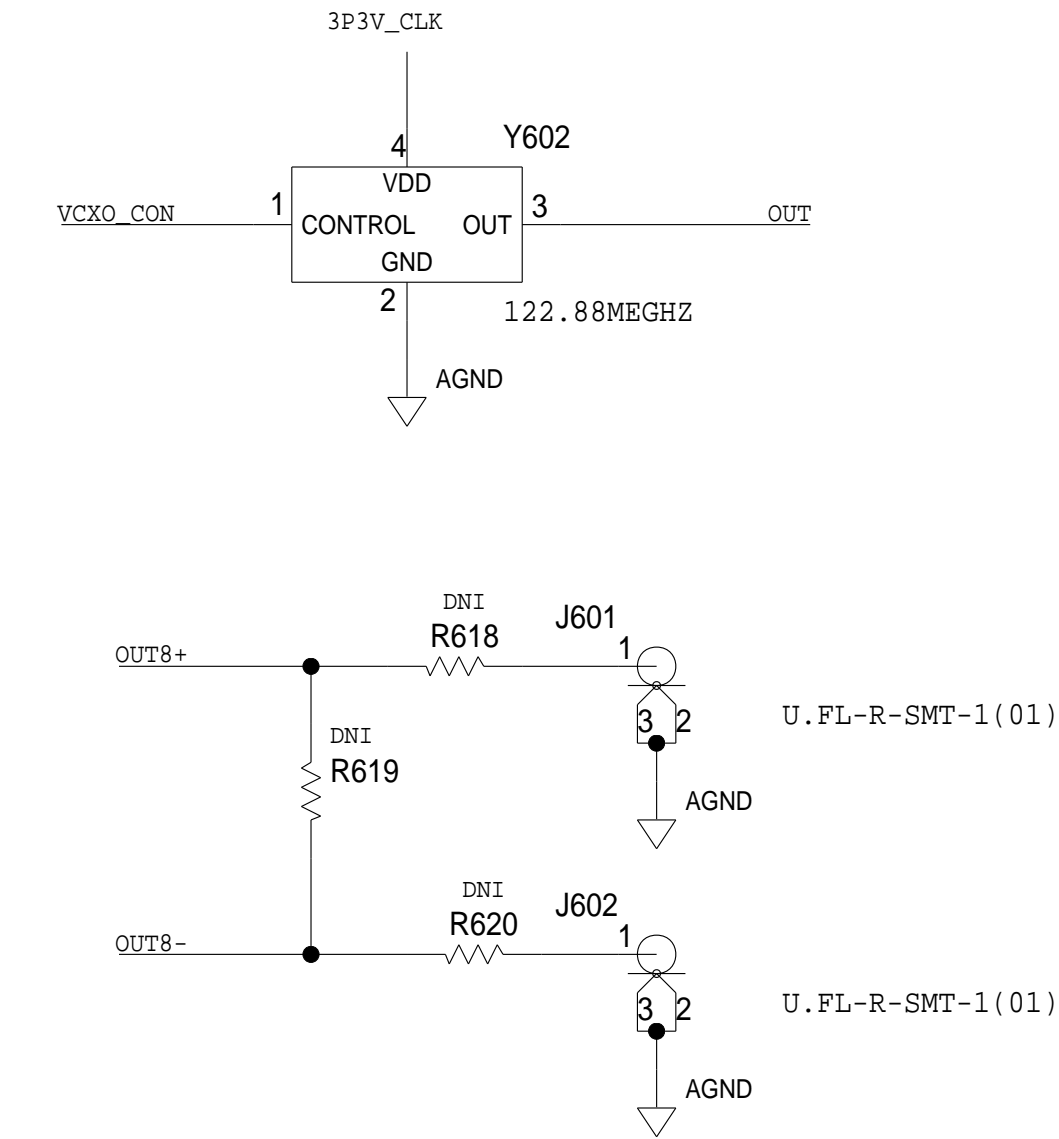
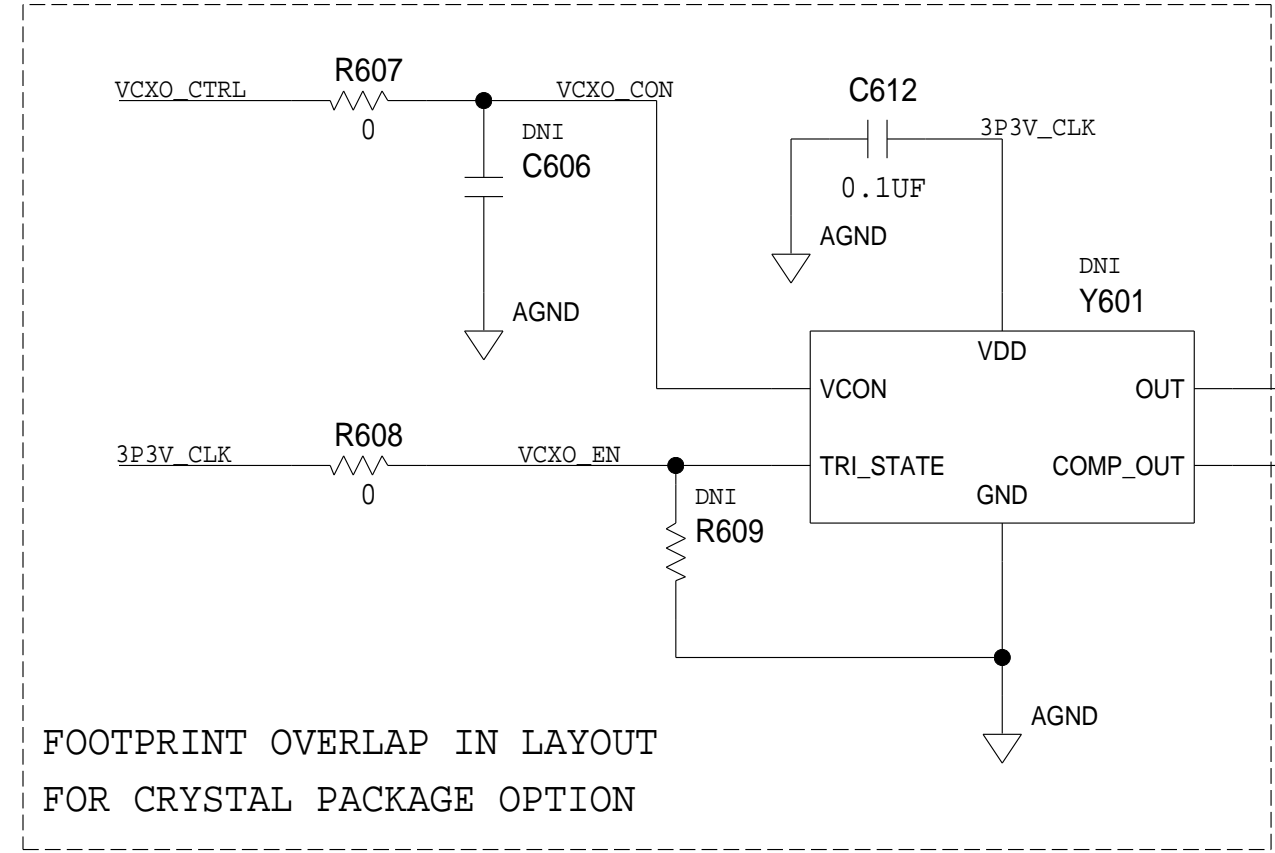


SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9029-MB			
DESIGN VIEW : ADRV9029-MB	DRAWING NO. 02_049168-06	REV C	
PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 5 OF 10

CLOCK DISTRIBUTION

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED


DEVICE CLK



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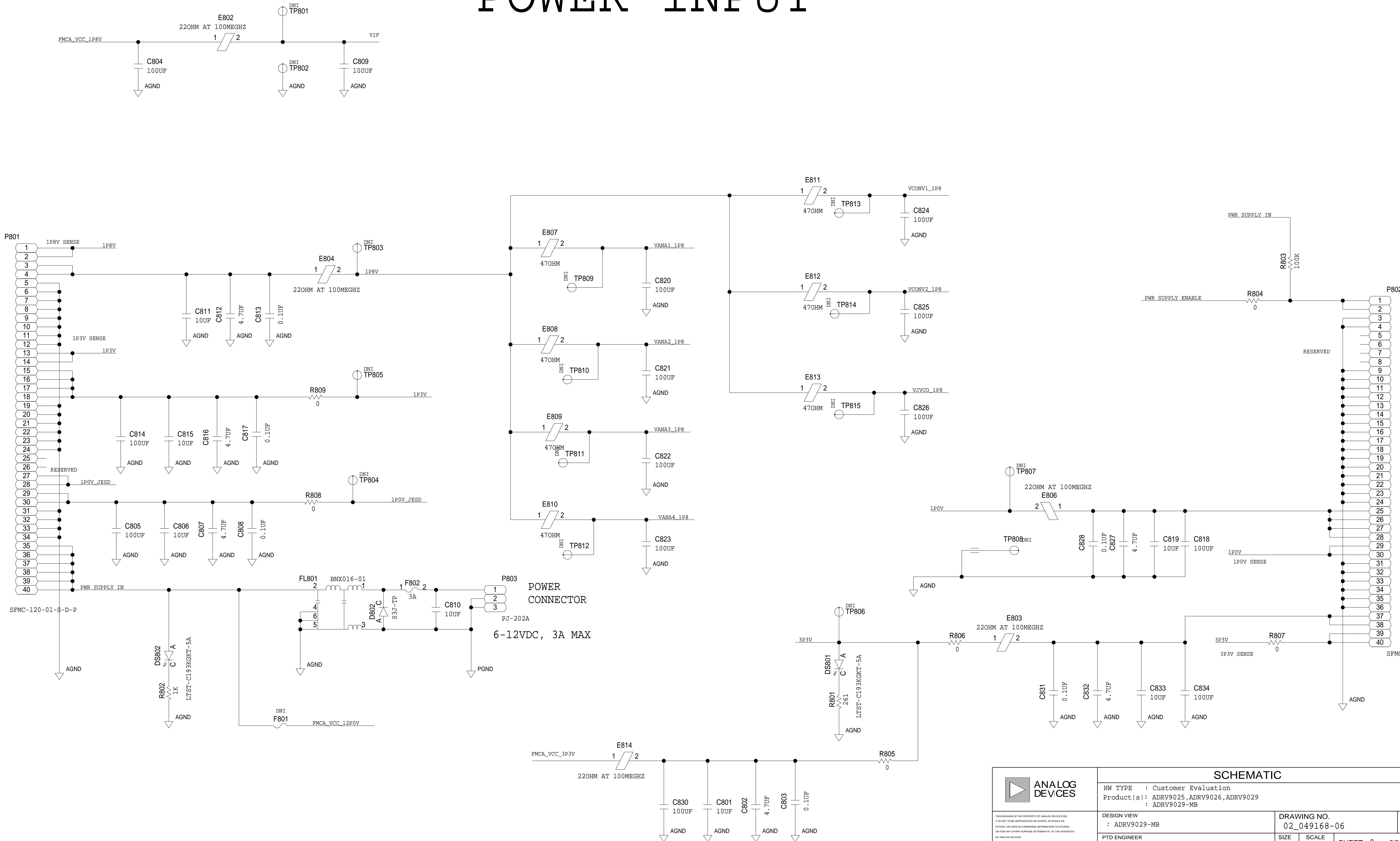
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9029-MB			
DESIGN VIEW : ADRV9029-MB	DRAWING NO. 02_049168-06	REV C	
PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 6 OF 10

[illegible]

 ANALOG DEVICES	SCHEMATIC				
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	PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 7	OF 10

POWER INPUT

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

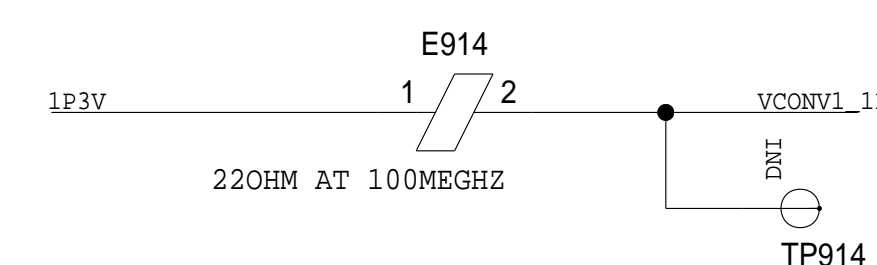
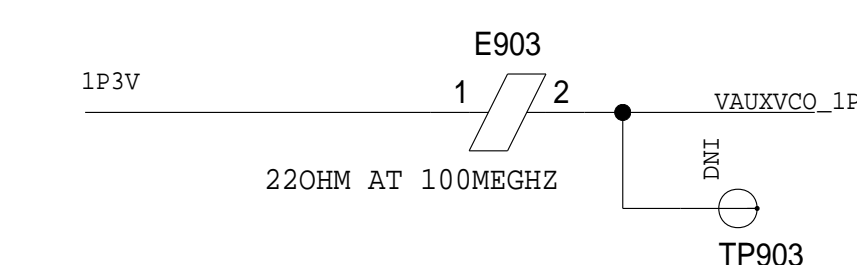
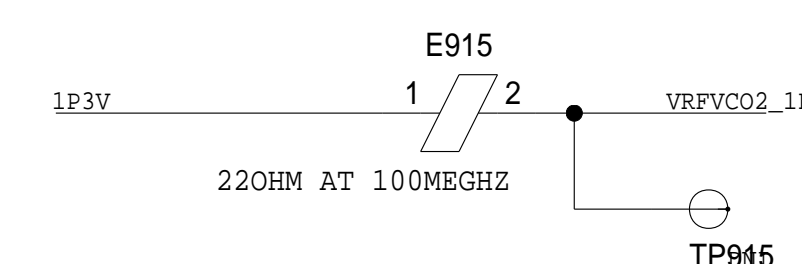
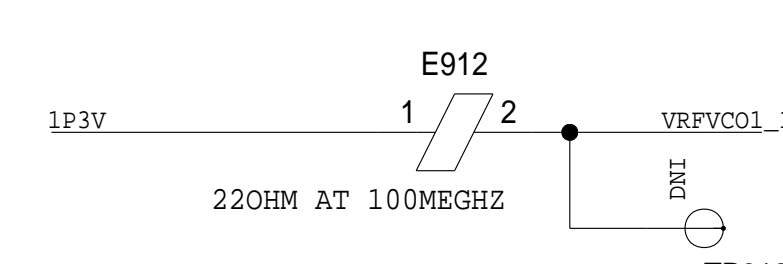
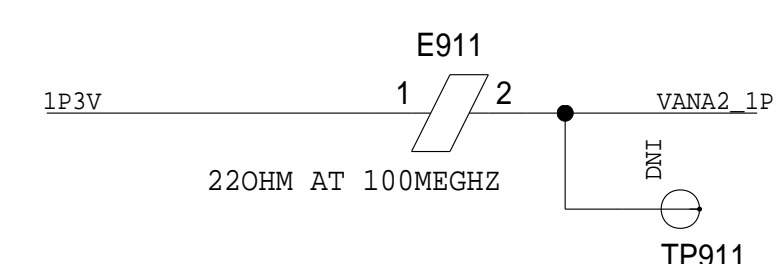
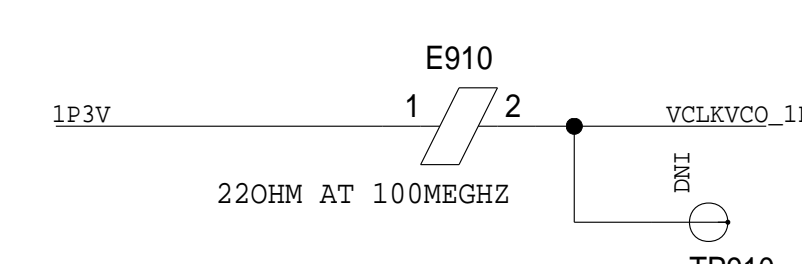
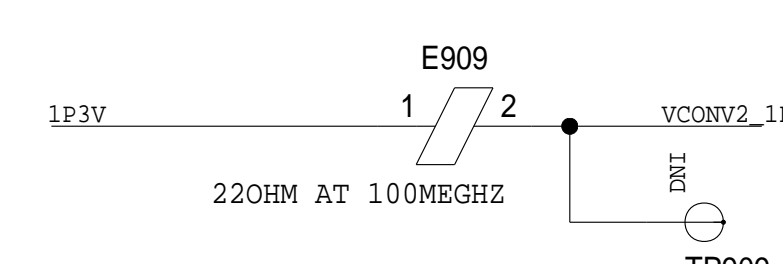
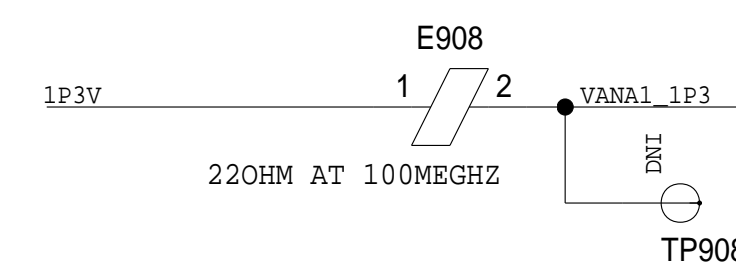
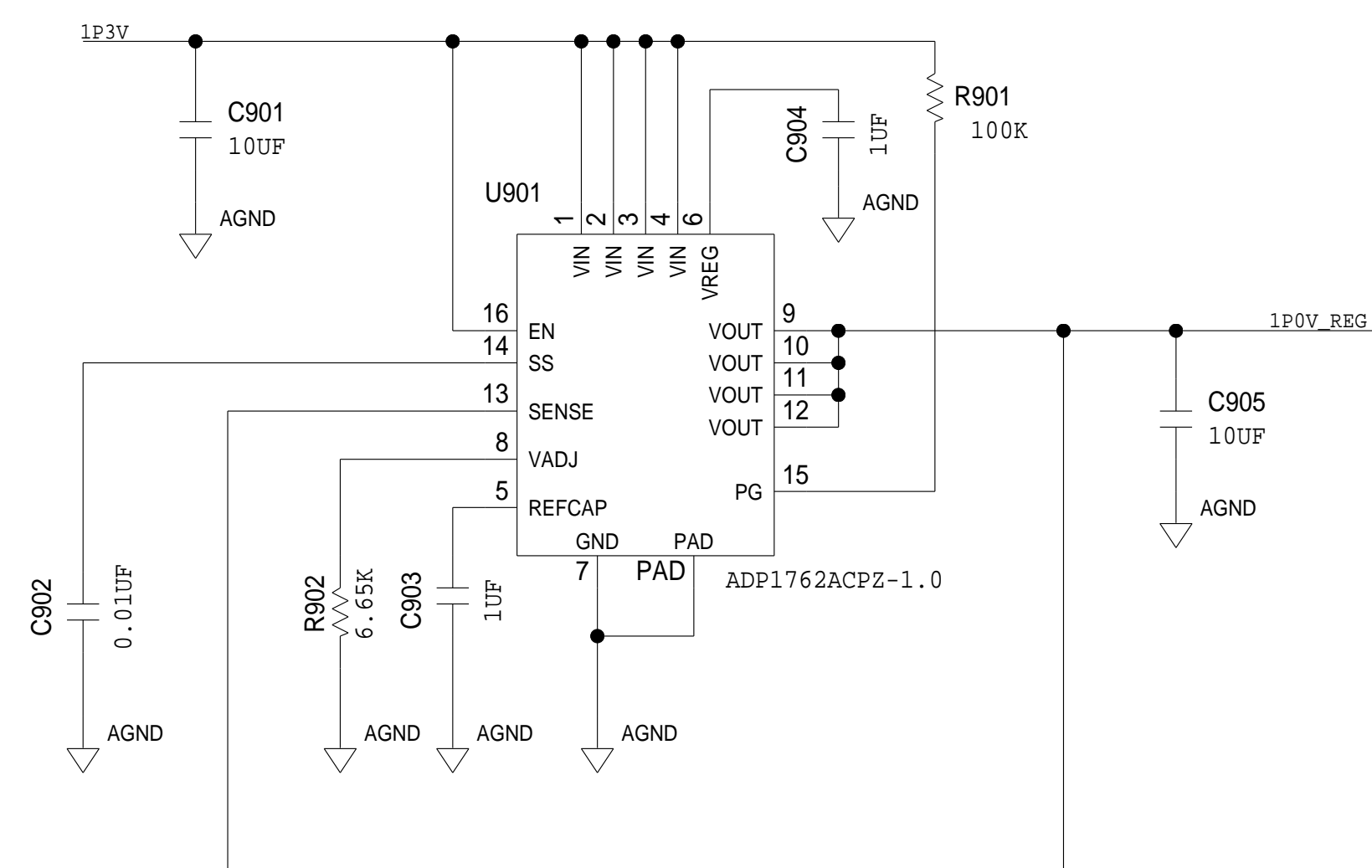


SCHEMATIC			
HW TYPE : Customer Evaluation Product (s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9029-MB			
DESIGN VIEW : ADRV9029-MB	DRAWING NO. 02_049168-06	REV C	
PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 8 OF 10

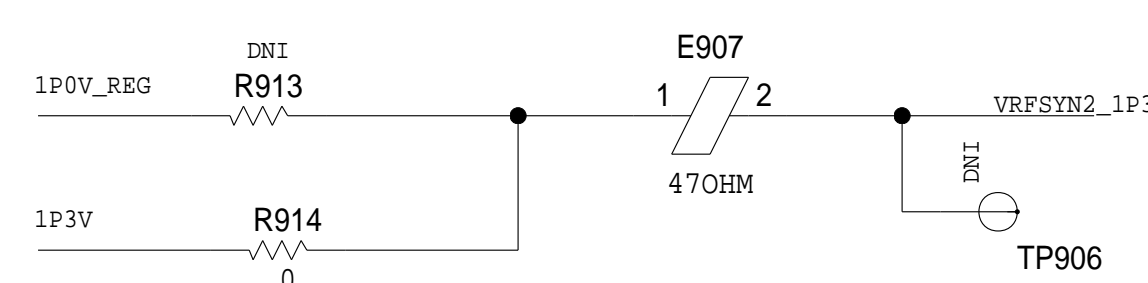
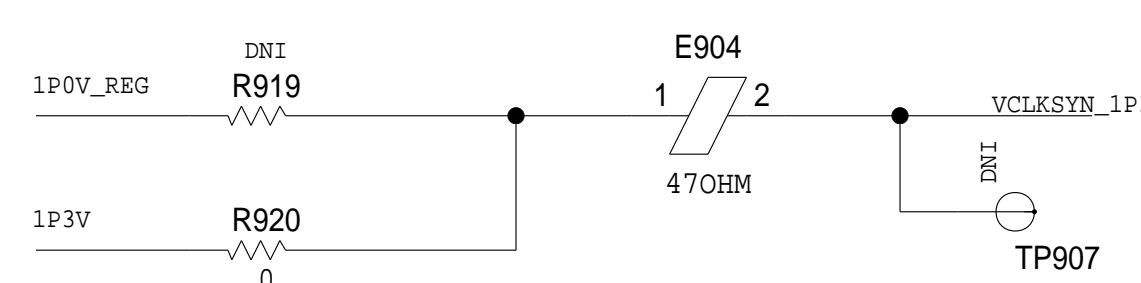
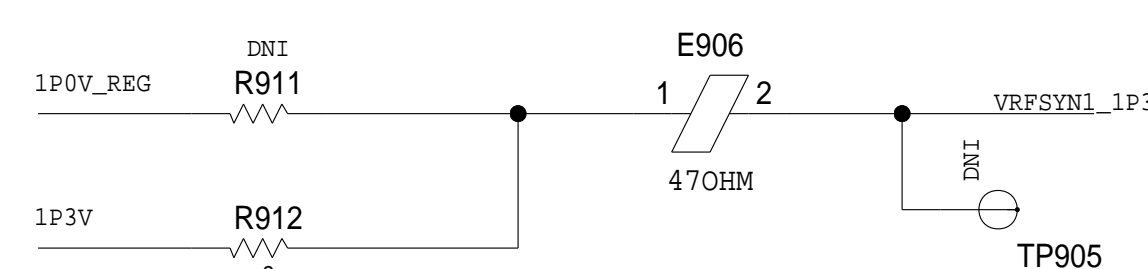
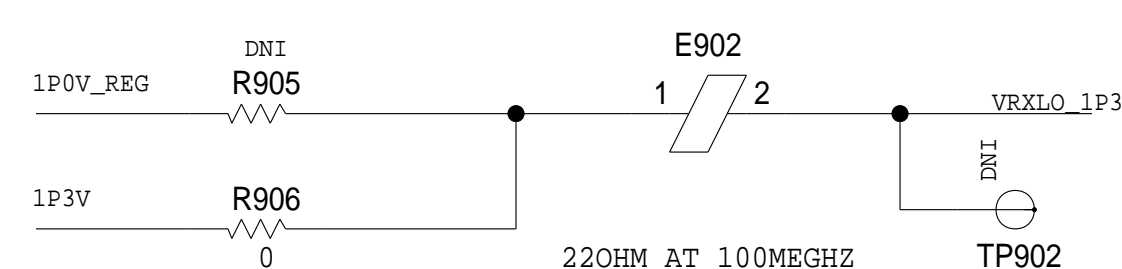
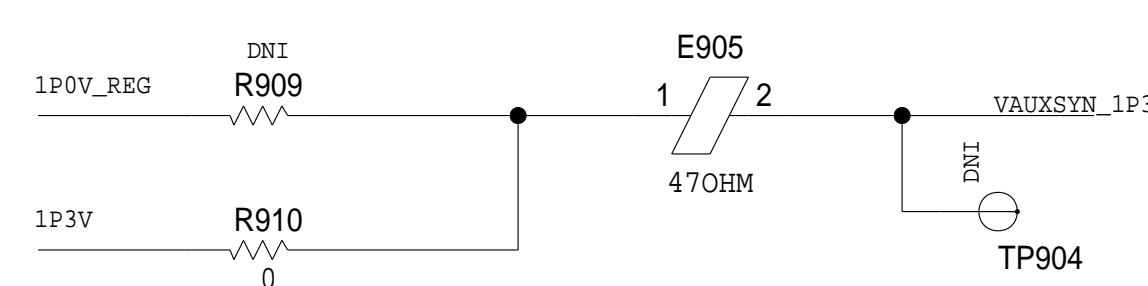
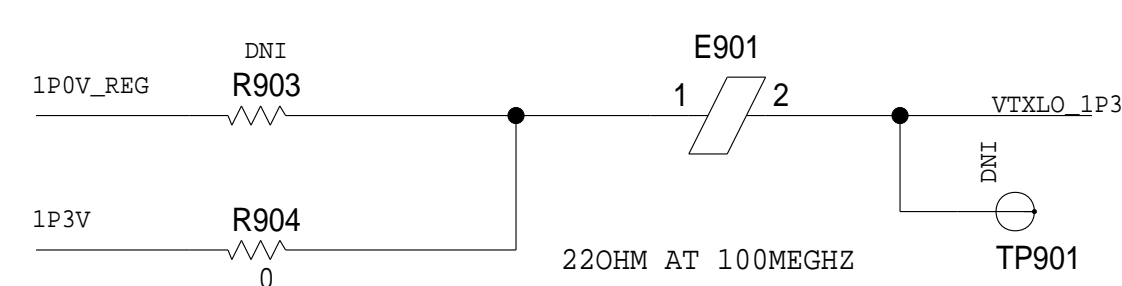
POWER DISTRIBUTION

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

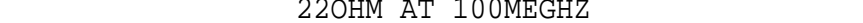
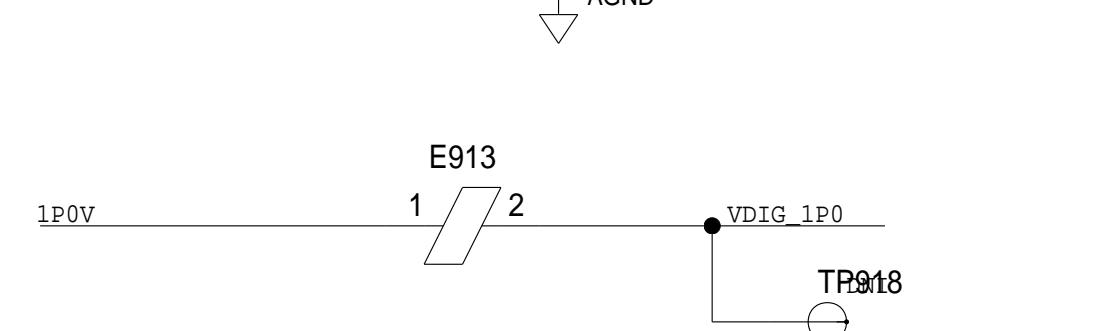
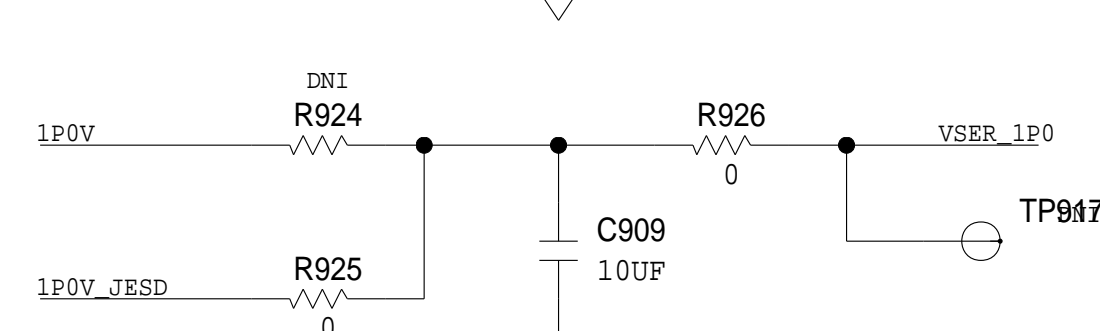
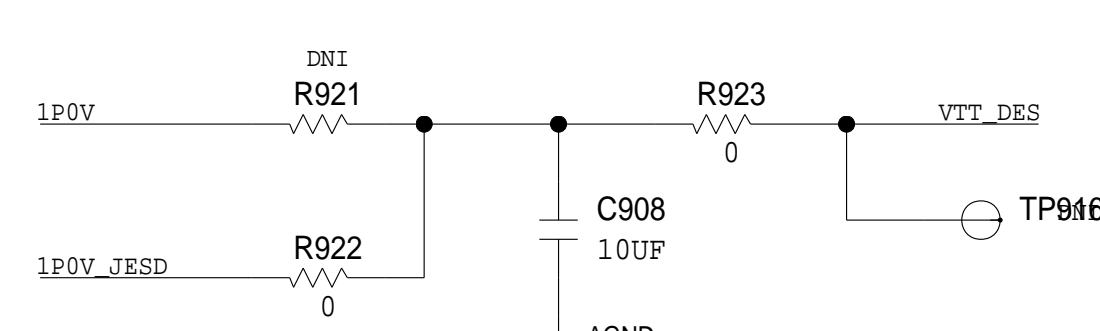
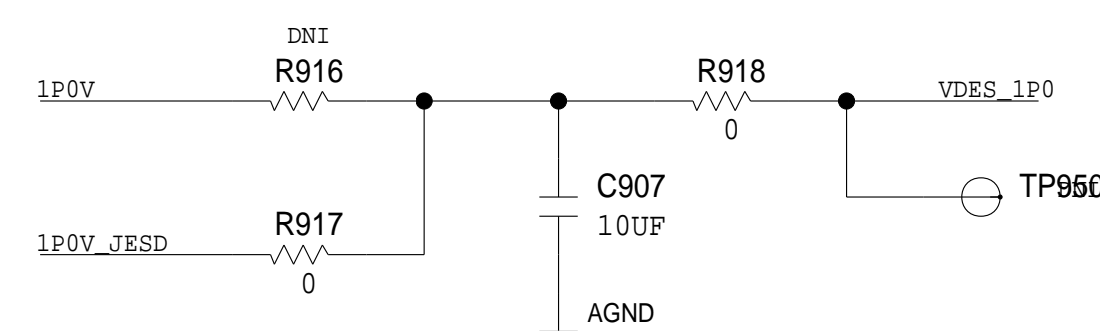
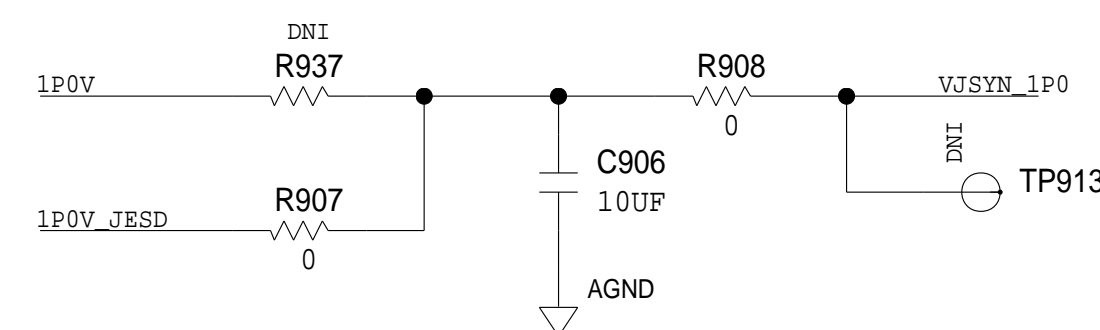
1.3V ANALOG SUPPLIES



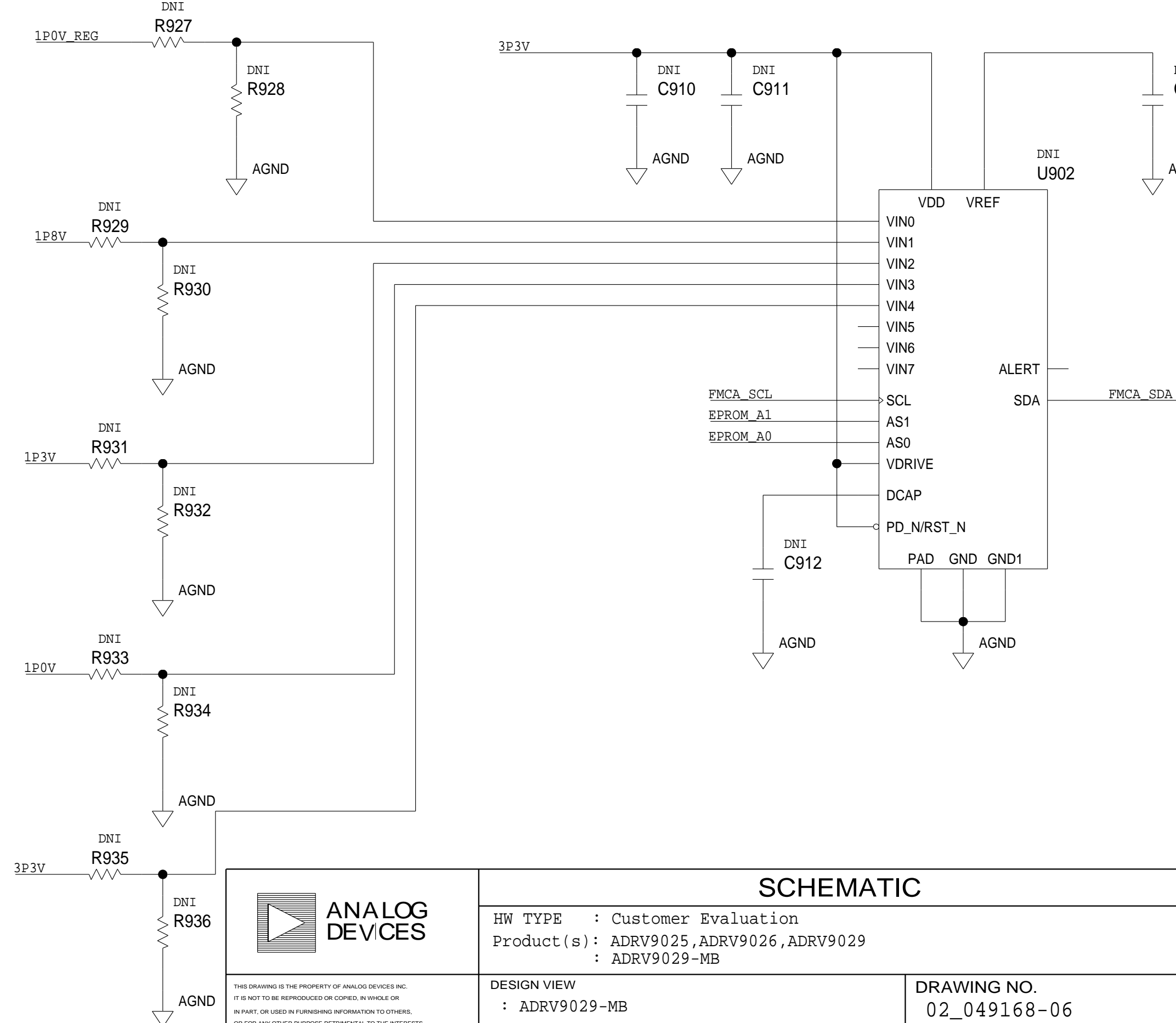
1.0V ANALOG SUPPLIES WITH 1.3V OPTION




1.0V DIGITAL SUPPLIES



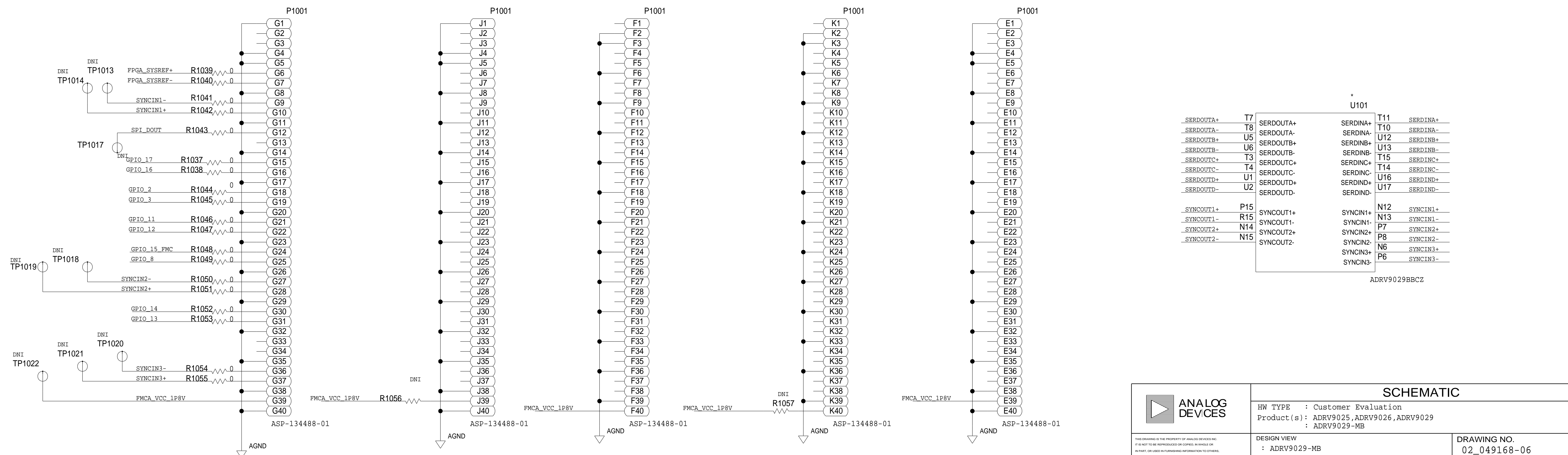
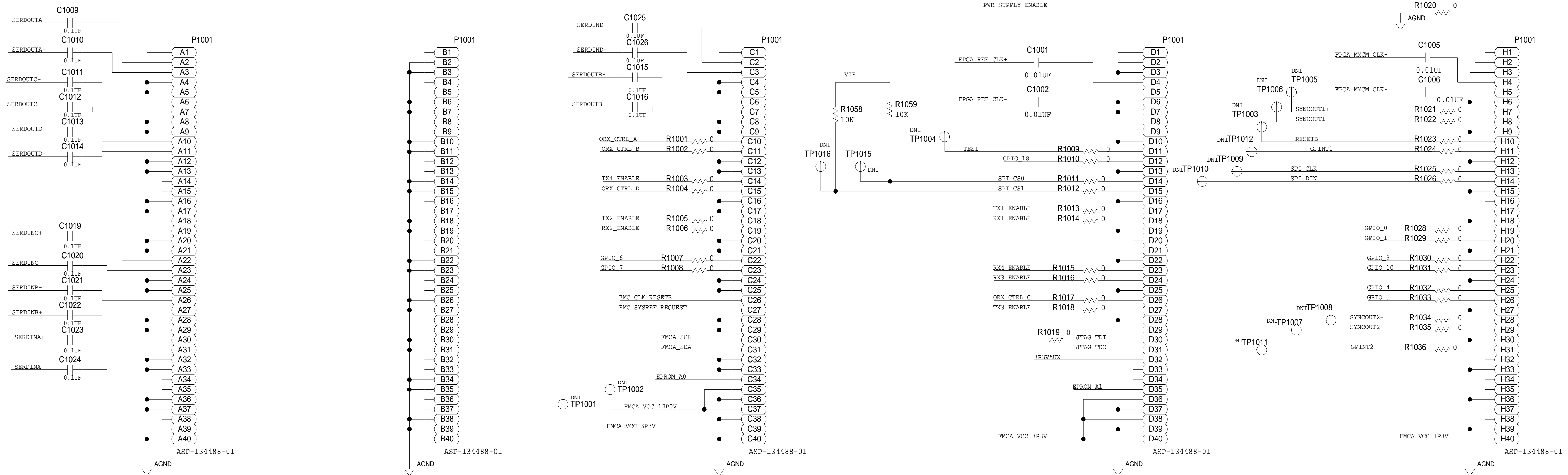
SUPPLY TEST MONITOR




<div><div>ANALOG DEVICES</div></div>		<div>SCHEMATIC</div> <div>HW TYPE : Customer Evaluation</div> <div>Product(s) : ADRV9025,ADRV9026,ADRV9029</div> <div>: ADRV9029-MB</div>					
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<div>THE EQUIPMENT BOMBS DESIGN MUST BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES INC.</div>		<div>PTD ENGINEER</div> <div>R. MYERS</div>		<div>SIZE</div> <div>D</div>		<div>SCALE</div> <div>1:1</div>	
				<div>SHEET 9</div>		<div>OF 10</div>	

FPGA CONNECTIONS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



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		<div>DESIGN VIEW</div> <div>: ADRV9029-MB</div>		<div>DRAWING NO.</div> <div>02_049168-06</div>	
<div>PTD ENGINEER</div> <div>R. MYERS</div>		<div>SIZE</div> <div>D</div>	<div>SCALE</div> <div>1:1</div>	<div>SHEET 10 OF 10</div>	